FORM PTO-1449 (Modified)			4 2003	SUR920030056US1	SERIAL NO. Unassigned 10/605960			
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE TATRACT STATEMENT David S. Collins, et al.								
(Use several sheets if necessary)			FILING DATE: 11/10/03 GROUP: 2825 Concurrently-Herewith Unassigned					
REFERENCE D	ESIGNATION	U.S	. PATE	ENT DOCUMENTS				
EXAMINER INITIALS	DOCUMENT NUMBER	DATE		NAME	CLAS	S SUBCLASS	FILING DATE (IF APPRO.)	
				·				
		FORE	GN PA	TENT DOCUMENTS	 	<u></u>	1	
	DOCUMENT NUMBER	DATE		COUNTRY	CLASS	SUBCLASS	TRANS	LATION NO
							1155	NO
OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)								
Sue E. Strang, et al., "A Design System for Auto-Generation of ESD Circuits", International Cadence Usergroup Conference, September 16-18, 2002, San Jose, California								
				· · · · · · · · · · · · · · · · · · ·				
EXAMINER	au Dinh		DATE CONSIDERED 1/6/06					
EXAMINER: In not in conforman	itial if reference considered.	lered, whether o Include copy o	or not ci	tation is in conformance worm with next communicati	ith MPEP (609. Draw line cant.	through c	itation if